

SRK INSTITUTE OF YECHNOLOGY, ENIKEPADU, JAYAWADA AUTONOMOUS

EXAMINATION CENTER

II B.TECH II SEMESTER (SRK23 REGULATION) REGULAR EXAMINATIONS, APRIL-2025

TIME TABLE

BRANCH		TIME: 02:00 PM TO 05:00 PM				
	28-04-2025 (Monday)	30.04.2025 (Wednesday)	02.05.2025 (Friday)	05.05.2025 (Monday)	07.05.2025 (Wednesday)	09.05.2025 (Friday)
CIVIL ENGINEERING (01-CE)	Managerial Economics and Financial Analysis	Engineering Geology	Concrete Technology	Structural Analysis	Hydraulics &Hydraulic Machinery	
ELECTRICAL AND ELECTRONICS ENGINEERING (02-EEE)	Managerial Economics and Financial Analysis	Analog Circuits	Power Systems-I	Induction and Synchronous Machines	Control Systems	
MECHANICAL ENGINEERING (03- ME)	Industrial Management	Complex Variables, Probability and Statistics	Manufacturing processes	Fluid Mechanics &Hydraulic Machines	Theory of Machines	·
ELECTRONICS & COMMUNICATION ENGINEERING (04-ECE)	Managerial Economics and Financial Analysis	Linear Control Systems	Electromagnetic Waves and Transmission Lines	Electronic Circuit Analysis	Analog Communications	
COMPUTER SCIENE & ENGINEERING (05-CSE)	Managerial Economics and Financial Analysis	Probability & Statistics	Operating Systems	Database Management Systems	Software Engineering	
INFORMATION TECHNOLOGY (12- IT)	Optimization Techniques	Probability & Statistics	Operating Systems	Database Management Systems	Software Engineering	







SRK INSTITUTE OF TECHNOLOGY, ENIKEPADU, V.JAYAWADA **AUTONOMOUS**

EXAMINATION CENTER

II B.TECH II SEMESTER (SRK23 REGULATION) REGULAR EXAMINATIONS, APRIL-2025

TIME TABLE

BRANCH -			DAY A	ND DATE	TIME: 02:00 PM TO 05:00 PM	
	28-04-2025 (Monday)	30.04.2025 (Wednesday)	02.05.2025 (Friday)	05.05.2025 (Monday)	07.05.2025 (Wednesday)	09.05.2025 (Friday)
CSE (Artificial Intelligence and Machine Learning) (42)	Optimization Techniques	Probability & Statistics	Machine Learning	Database Management Systems	Digital Logic and Computer Organization	
CSE (Data Science) (44)	Optimization Techniques	Probability & Statistical Methods for Data Science	Data Engineering	Database Management Systems	Digital Logic and Computer Organization	
ECE (VLISI) (66)	Managerial Economics and Financial Analysis	Analog Circuits	Computer Architecture and Organization	Analog & Digital Communications	Introduction to Micro fabrication	Introduction to VLSI life cycle

NOTE: i. ANY OMISSIONS OR CLASHES IN THIS TIMETABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY. ii. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED ASUSUAL

CONTROLLER OF EXAMINATIONS SRK Institute of Technology Enikepadu, VIJAYAWADA

CSM. BEES CE EVE WD.

S.R.K. INSTITUTE OF TECHNOLOG

SRK institute of Technology Enikepadu, VIJAYAWADA